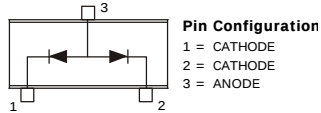
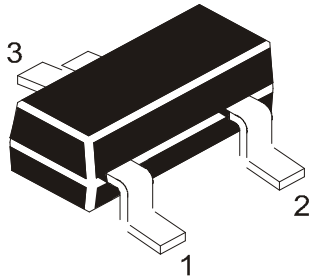


SILICON PLANAR DUAL SWITCHING DIODES

BAW74



SOT-23
Formed SMD Package

Marking

BAW74 - JD

High-Speed Switching Dual Diodes, Common Anode

ABSOLUTE MAXIMUM RATINGS

DESCRIPTION	SYMBOL	VALUE	UNIT
Maximum Repetitive Reverse Voltage	V_{RRM}	50	V
Average Rectified Forward Current	$I_{F(AV)}$	200	mA
Power Dissipation $T_a=25^\circ\text{C}$	P_D	350	mW
Non Repetitive Peak Forward Surge Current	I_{FSM}		
Pulse Width=1s		1	A
Pulse Width=1ms		2	A
Storage Temperature Range	T_{stg}	- 55 to +150	$^\circ\text{C}$
Operating Junction Temperature	T_j	150	$^\circ\text{C}$

THERMAL RESISTANCE

Junction to Ambient in free air	$R_{th(j-a)}$	357	$^\circ\text{C/W}$
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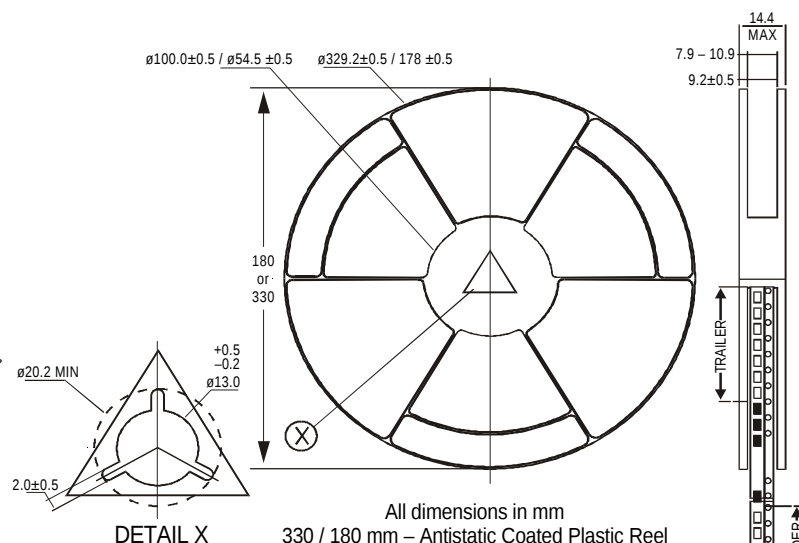
ELECTRICAL CHARACTERISTICS ($T_a=25^\circ\text{C}$ unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	MAX	UNIT
Breakdown Voltage	V_R	$I_R = 100\mu\text{A}$	50		V
Forward Voltage	$*V_F$	$I_F = 100\text{mA}$		1	V
Reverse Current	$*I_R$	$V_R=50\text{V}$		100	nA
		$V_R=50\text{V}, T_a=150^\circ\text{C}$		100	μA
Total Capacitance	C_T	$V_R=0\text{V}, f=1\text{MHz}$		2	pF
Reverse Recovery Time	t_{rr}	$I_F=I_R=10\text{mA}, I_{RR}=1\text{mA}, R_L=100\Omega$		4	ns

*Pulse Test : Pulse Width =300ms, Duty Cycle=2%

SOT-23 Package Reel Information

Reel Specifications for W Packing (13") and 7"



No. of Devices

1. The bandolier of 330 mm reel contains at least 10,000 devices.
2. The bandolier of 180 mm reel contains at least 3,000 devices.
3. No more than 0.5% missing devices / reel. 50 empty compartments for 330 mm reel. 15 empty compartments for 180 mm reel.
4. Three consecutive empty places might be found provided this gap is followed by 6 consecutive devices.
5. The carrier tape (leader) starts with at least 75 empty positions (equivalent to 330 mm). In order to fix the carrier tape a self adhesive tape of 20 to 50 mm is applied. At the end of the bandolier at least 40 empty positions (equivalent to 160 mm) are there.

Technical drawing of a multi-layer printed circuit board (PCB) showing dimensions and tolerances. The drawing includes a cross-section on the left and a top view on the right. Dimensions are given in millimeters with tolerances in parentheses.

Dimensions and Tolerances:

- Top View Dimensions:**
 - Overall width: 4.0 ± 0.1
 - Distance from left edge to first hole center: 2.0 ± 0.05
 - Distance between hole centers: 4.0 ± 0.1
 - Distance from last hole center to right edge: 1.55 ± 0.05
 - Overall length: 8.0 ± 0.1
 - Distance from top edge to first hole center: 1.75 ± 0.1
 - Distance from first hole center to bottom edge: 3.5 ± 0.05
 - Distance from bottom edge to last hole center: 5.75 ± 0.05 (MAX)
 - Distance from right edge to last hole center: 1.0 ± 0.05
 - Distance from bottom edge to last hole center: 3.15 ± 0.10
- Cross-section Dimensions:**
 - Overall width: 2.77 ± 0.10
 - Distance from left edge to center: 1.22 ± 0.1
 - Distance from center to right edge: 1.6 ± 0.1 (MAX)

Direction of Unreeling: Indicated by a dashed line and an arrow pointing to the right.

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight /Qty	Size	Qty	Size	Qty	Gr Wt
SOT-23 T&R	3K/reel	136 gm/3K pcs	3" x 7.5" x 7.5"	12 K	17" x 15" x 13.5"	192 K	12 kgs
			9" x 9" x 9"	51 K	19" x 19" x 19"	408 K	28 kgs
	10K/reel	415 gm/10K pcs	13" x 13" x 0.5"	10 K	17" x 15" x 13.5"	300 K	16 kgs

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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